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FOREWORD

This issue of the Journal of Integrated Circuits and Systems (JICS) includes papers on analog design, design for yield, design for security and dynamically reconfigurable architectures. These papers have been selected from the presentations given at SBCCI2009 (22nd Symposium on Integrated Circuits and Systems Design), which has been held in Natal, Brazil in 2009. Among the contributions presented at SBCCI2009, only a few best rated by the reviewers were selected by the JICS Editorial Board and have been invited to submit an extended version. These papers have been submitted to usual reviewing process with the help of external experts. In this issue also spontaneous submissions have been considered.

We would like to thank the authors for their effort in preparing these high quality papers, as well as the reviewers for their help on paper selection, which guarantees the scientific level of this issue. We sincerely hope that JICS readers will enjoy these contributions.

João Antonio Martino
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JICS - Journal of Integrated Circuits and Systems

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After acceptance, the author will be notified about the final paper format to be submitted for printing.