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## FOREWORD

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This issue of the Journal of Integrated Circuits and Systems (JICS) features a very broad content, including papers on analog and digital design, presenting novel solutions for circuit synthesis, energy efficiency, memory architectures and processor modeling and also proposing new approaches for wireless communication, motion estimation and neural networks applications.

Six of these papers have been selected from the presentations given at SBCCI2011 (24th Symposium on Integrated Circuits and Systems Design), which has been held in João Pessoa, Brazil in 2011. Among the contributions presented at the Symposium, only a few best rated were selected by the JICS Editorial Board and have been invited to submit an extended version to the Journal. These papers have been reviewed by external experts and have been accepted for the special section on best SBCCI2011 papers. In addition to the best papers presented at the conference, one spontaneous submission passed through the usual reviewing process and has been accepted as a regular paper.

We would like to thank the authors for their effort in preparing these high quality papers, as well as the reviewers for their help on paper selection, which guarantees the scientific level of this issue. We sincerely hope that JICS readers will enjoy these contributions.

Marcelo Lubaszewski  
*JICS Editor-in-chief*

Marcelo Antonio Pavanello  
*JICS Co-Editor*